**OPEN** 

**GREEN** 

**STABLE** 

Less Heat, Less Power Consumption Robust Design, Quality Parts

Stable and Reliable Solution

ver/Workstation

GNR2D16-2T

User Manual



Version 1.00
Published Feb. 2025

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"Perchlorate Material-special handling may apply, see <a href="www.dtsc.ca.gov/hazardouswaste/perchlorate">www.dtsc.ca.gov/hazardouswaste/perchlorate</a>"

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This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

- (1) this device may not cause harmful interference, and
- (2) this device must accept any interference received, including interference that may cause undesired operation.

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following

#### measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.



ASRock Rack INC. hereby declares that this device is in compliance with the essential requirements and other relevant provisions of related Directives. Full text of EU declaration of conformity is available at: http://www.asrockrack.com

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DO NOT throw the motherboard in municipal waste. This product has been designed to enable proper reuse of parts and recycling. This symbol of the crossed out wheeled bin indicates that the product (electrical and electronic equipment) should not be placed in municipal waste. Check local regulations for disposal of electronic products.

## Contents

Cnap	oter 1 Introduction	1
1.1	Package Contents	1
1.2	Specifications	2
1.3	Unique Features	5
1.4	Motherboard Layout	6
1.5	Onboard LED Indicators	9
1.6	I/O Panel	11
1.7	Block Diagram	13
Chap	oter 2 Installation	14
2.1	Screw Holes	14
2.2	Pre-installation Precautions	15
2.3	Installing the CPU and Heatsink	16
2.4	Installation of Memory Modules (DIMM)	22
2.5	Expansion Slots (PCI Express Slots)	25
2.6	Onboard Headers and Connectors	26
2.7	Unit Identification purpose LED/Switch	33
2.8	Dr. Debug	34
2.9	Dual LAN and Teaming Operation Guide	40
2.10	M.2 SSD Module Installation Guide	41
Chap	oter 3 UEFI Setup Utility	43
3.1	Introduction	43
3.1.1	UEFI Menu Bar	43
3.1.2	Navigation Keys	44

3.2	Main Screen	45
3.2.1	Mother Board Information	46
3.2.2	Processor Information	46
3.2.3	Memory Information	47
3.3	Advanced Screen	48
3.3.1	CPU Configuration	49
3.3.2	Platform Power Configuration	52
3.3.3	DRAM Configuration	55
3.3.4	Chipset Configuration	57
3.3.5	NVMe Configuration	59
3.3.6	ACPI Configuration	60
3.3.7	USB Configuration	61
3.3.8	Super IO Configuration	62
3.3.9	Serial Port Console Redirection	63
3.3.10	H/W Monitor	66
3.3.11	Network Stack Configuration	67
3.3.12	Intel VMD Technology	69
3.3.13	Driver Health	70
3.3.14	TIs Auth Configuration	71
3.3.15	Intel(R) Ethernet Controller	72
3.3.16	VLAN Configuration	74
3.3.17	Instant Flash	75
3.4	Server Mgmt	76
3.4.1	BMC Network Configuration	78

3.4.2	DNS Configuration	80
3.4.3	System Event Log	82
3.4.4	BMC Tools	83
3.5	Event Logs	84
3.6	Security	85
3.6.1	Expert Key Management	87
3.7	Boot Screen	91
3.8	Exit Screen	92
Chap	ter 4 Software Support	93
4.1		
7.1	Download and Install Operating System	93
4.2	Download and Install Operating System  Download and Install Software Drivers	93 93
4.2 4.3	Download and Install Software Drivers	93
4.2 4.3	Download and Install Software Drivers  Contact Information	93 93
4.2 4.3 <b>Chap</b>	Download and Install Software Drivers  Contact Information  ter 5 Troubleshooting	93 93 <b>94</b>

# English

# **Chapter 1 Introduction**

Thank you for purchasing ASRock Rack *GNR2D16-2T* motherboard, a reliable motherboard produced under ASRock Rack's consistently stringent quality control. It delivers excellent performance with robust design conforming to ASRock Rack's commitment to quality and endurance.

In this manual, chapter 1 and 2 contains introduction of the motherboard and step-by-step guide to the hardware installation. Chapter 3 and 4 contains the configuration guide to BIOS setup and information of the software support.



Because the motherboard specifications and the BIOS software might be updated, the content of this manual will be subject to change without notice. In case any modifications of this manual occur, the updated version will be available on ASRock Rack website without further notice. Find the latest memory and CPU support lists on ASRock Rack website as well. ASRock Rack's Website: <a href="https://www.ASRockRack.com">www.ASRockRack.com</a>

About this motherboard technical support, please visit the website for specific information <a href="http://www.asrockrack.com/support/">http://www.asrockrack.com/support/</a>

## 1.1 Package Contents

- ASRock Rack GNR2D16-2T motherboard (EEB form factor: 12.24" x 13.3", 31.1cm x 33.8cm)
- · Quick installation guide
- · 1 I/O shield
- 1 Mini SAS HD to 4\* SATA cable (60cm)
- · 2 screws for M.2 sockets
- 4 x CPU Non-Fabric carriers (2xE2A, 2xE2B)



If any items are missing or appear damaged, contact the authorized dealer.

# 1.2 Specifications

MB Physical Status	GNR2D16-2T				
Dimension   12.24" x 13.3" (31.1cm x 33.8cm)					
Dimension   12.24" x 13.3" (31.1cm x 33.8cm)	•				
Processor System   CPU					
CPU					
Socket	•	Supports Intol® Voon® 4700E corios processors			
Chipset System on Chip  System Memory Supported 8+8 DIMM slots (1DPC)  DIMM Quantity Supported Type DDR5 RDIMM Max. Capacity RDIMM: 128GB per DIMM Max. DIMM RDIMM: 6400MT/s Frequency Voltage 1.1V Note memory support is to be validated  PCIe Expansion Slots  PCIe x16 SLOT1: PCIe5.0/CXL2.0 x16 [CPU1] SLOT2: PCIe5.0/CXL2.0 x16 [CPU1] SLOT3: PCIe5.0/CXL2.0 x16 [CPU1] SLOT3: PCIe5.0/CXL2.0 x16 [CPU1] SLOT5: PCIe5.0/CXL2.0 x16 [CPU1] PCIe x8 SLOT6: PCIe5.0/CXL2.0 x16 [CPU1] PCIe x8 SLOT6: PCIe5.0/CXL2.0 x8 [CPU0] Other PCIe Expansion Connectors M.2 2 M-key (PCIe5.0 x2), supports 22110/2280 form factor [CPU0] MCIO 1 MCIO (PCIe5.0/CXL2.0 x8) [CPU0] MCIO 1 MCIO (PCIe5.0/CXL2.0 x8) [CPU0] SATA/SAS Storage Additional 8 SATA 6Gb/s by ASM1164: 2 Mini-SAS HD SATA Controller Ethernet Additional GbE 2 RJ45 (10GbE) by Intel* X710-AT2 Controller  Graphics Controller ASPEED AST2600: 1 DB15 (VGA)  Rear I/O UID Button/ LED Video Output 1 DB15 (VGA) USB 2 Type-A (USB3.2 Gen1)					
System Memory   Supported   8+8 DIMM slots (1DPC)		` '			
Supported   DIMM Quantity	-	j dystem on emp			
DIMM Quantity		8+8 DIMM slots (1DPC)			
Supported Type   DDR5 RDIMM		(			
Max. Capacity   per DIMM		DDR5 RDIMM			
Description   Proceedings   Process   Proces					
Max. DIMM   RDIMM: 6400MT/s	- ,				
Voltage		RDIMM: 6400MT/s			
Voltage					
Note		1 1V			
PCIe Expansion Slots  PCIe x16  SLOT1: PCIe5.0/CXL2.0 x16 [CPU1]  SLOT2: PCIe5.0/CXL2.0 x16 [CPU0]  SLOT3: PCIe5.0/CXL2.0 x16 [CPU1]  SLOT4: PCIe5.0/CXL2.0 x16 [CPU0]  SLOT5: PCIe5.0/CXL2.0 x16 [CPU1]  PCIe x8  SLOT6: PCIe5.0/CXL2.0 x8 [CPU0]  Other PCIe Expansion Connectors  M.2  2 M-key (PCIe5.0 x2), supports 22110/2280 form factor [CPU0]  MCIO  1 MCIO (PCIe5.0/CXL2.0 x8) [CPU0]  SATA/SAS Storage  Additional  SATA Gob/s by ASM1164: 2 Mini-SAS HD  SATA Controller  Ethernet  Additional GbE Controller  Graphics  Controller  ASPEED AST2600: 1 DB15 (VGA)  Rear I/O  UID Button/ LED  Video Output  1 DB15 (VGA)  USB  2 Type-A (USB3.2 Gen1)		1 1			
PCIe x16  SLOT1: PCIe5.0/CXL2.0 x16 [CPU1]  SLOT2: PCIe5.0/CXL2.0 x16 [CPU0]  SLOT3: PCIe5.0/CXL2.0 x16 [CPU0]  SLOT4: PCIe5.0/CXL2.0 x16 [CPU0]  SLOT5: PCIe5.0/CXL2.0 x16 [CPU1]  PCIe x8  SLOT6: PCIe5.0/CXL2.0 x8 [CPU0]  Other PCIe Expansion Connectors  M.2  2 M-key (PCIe5.0 x2), supports 22110/2280 form factor [CPU0]  MCIO  I MCIO (PCIe5.0/CXL2.0 x8) [CPU0]  SATA/SAS Storage  Additional  SATA Controller  Ethernet  Additional GbE Controller  Graphics  Controller  ASPEED AST2600:  1 DB15 (VGA)  Rear I/O  UID Button/ LED  Video Output  1 DB15 (VGA)  USB  2 Type-A (USB3.2 Gen1)	PCIe Expansion S				
SLOT2: PCIe5.0/CXL2.0 x16 [CPU0]     SLOT3: PCIe5.0/CXL2.0 x16 [CPU1]     SLOT4: PCIe5.0/CXL2.0 x16 [CPU1]     SLOT5: PCIe5.0/CXL2.0 x16 [CPU1]     PCIe x8	*				
SLOT3: PCIe5.0/CXL2.0 x16 [CPU1]     SLOT4: PCIe5.0/CXL2.0 x16 [CPU0]     SLOT5: PCIe5.0/CXL2.0 x16 [CPU1]     PCIe x8					
SLOT4: PCIe5.0/CXL2.0 x16 [CPU0]     SLOT5: PCIe5.0/CXL2.0 x16 [CPU1]     PCIe x8					
SLOT5: PCIe5.0/CXL2.0 x16 [CPU1]     PCIe x8					
PCIe x8 SLOT6: PCIe5.0/CXL2.0 x8 [CPU0]  Other PCIe Expansion Connectors  M.2 2 M-key (PCIe5.0 x2), supports 22110/2280 form factor [CPU0]  MCIO 1 MCIO (PCIe5.0/CXL2.0 x8) [CPU0]  SATA/SAS Storage  Additional 8 SATA 6Gb/s by ASM1164: 2 Mini-SAS HD  SATA Controller  Ethernet  Additional GbE Controller  Graphics  Controller ASPEED AST2600: 1 DB15 (VGA)  Rear I/O  UID Button/ 1 UID button w/ LED  LED  Video Output 1 DB15 (VGA)  USB 2 Type-A (USB3.2 Gen1)					
Other PCIe Expansion Connectors  M.2	PCIe x8				
MCIO 1 MCIO (PCIe5.0/CXL2.0 x8) [CPU0]  SATA/SAS Storage Additional 8 SATA 6Gb/s by ASM1164: 2 Mini-SAS HD  SATA Controller  Ethernet Additional GbE Controller  Graphics Controller ASPEED AST2600: 1 DB15 (VGA)  Rear I/O  UID Button/ 1 UID button w/ LED  LED  Video Output 1 DB15 (VGA)  USB 2 Type-A (USB3.2 Gen1)	Other PCIe Expan	nsion Connectors			
SATA/SAS Storage Additional 8 SATA 6Gb/s by ASM1164: 2 Mini-SAS HD SATA Controller Ethernet Additional GbE Controller Graphics Controller ASPEED AST2600: 1 DB15 (VGA)  Rear I/O UID Button/ 1 UID button w/ LED LED Video Output 1 DB15 (VGA) USB 2 Type-A (USB3.2 Gen1)	M.2	2 M-key (PCIe5.0 x2), supports 22110/2280 form factor [CPU0]			
Additional 8 SATA 6Gb/s by ASM1164: 2 Mini-SAS HD  SATA Controller  Ethernet  Additional GbE 2 RJ45 (10GbE) by Intel® X710-AT2  Controller  Graphics  Controller ASPEED AST2600: 1 DB15 (VGA)  Rear I/O  UID Button/ 1 UID button w/ LED  LED  Video Output 1 DB15 (VGA)  USB 2 Type-A (USB3.2 Gen1)	MCIO	1 MCIO (PCIe5.0/CXL2.0 x8) [CPU0]			
Ethernet  Additional GbE   2 RJ45 (10GbE) by Intel® X710-AT2  Controller   Graphics  Controller   ASPEED AST2600:	SATA/SAS Storage	e			
Ethernet  Additional GbE   2 RJ45 (10GbE) by Intel® X710-AT2  Controller  Graphics  Controller   ASPEED AST2600: 1 DB15 (VGA)  Rear I/O  UID Button/ 1 UID button w/ LED  LED   Video Output	Additional	8 SATA 6Gb/s by ASM1164: 2 Mini-SAS HD			
Additional GbE Controller  Graphics  Controller  ASPEED AST2600: 1 DB15 (VGA)  Rear I/O  UID Button/ LED  Video Output  1 DB15 (VGA)  USB  2 Type-A (USB3.2 Gen1)	SATA Controller				
Controller  Graphics  Controller  ASPEED AST2600: 1 DB15 (VGA)  Rear I/O  UID Button/ LED  Video Output  1 DB15 (VGA)  USB  2 Type-A (USB3.2 Gen1)	Ethernet				
Graphics	Additional GbE	2 RJ45 (10GbE) by Intel® X710-AT2			
ASPEED AST2600:   1 DB15 (VGA)     Rear I/O	Controller				
1 DB15 (VGA)   Rear I/O	Graphics				
Rear I/O  UID Button/ 1 UID button w/ LED  LED  Video Output 1 DB15 (VGA)  USB 2 Type-A (USB3.2 Gen1)	Controller	ASPEED AST2600:			
UID Button/ 1 UID button w/ LED  LED  Video Output 1 DB15 (VGA)  USB 2 Type-A (USB3.2 Gen1)		1 DB15 (VGA)			
LED           Video Output         1 DB15 (VGA)           USB         2 Type-A (USB3.2 Gen1)	Rear I/O				
Video Output 1 DB15 (VGA) USB 2 Type-A (USB3.2 Gen1)	UID Button/	1 UID button w/ LED			
USB 2 Type-A (USB3.2 Gen1)	LED				
	Video Output	1 DB15 (VGA)			
RJ45 2 RJ45 (10GbE), 1 dedicated IPMI	USB	2 Type-A (USB3.2 Gen1)			
	RJ45	2 RJ45 (10GbE), 1 dedicated IPMI			

Temperature Fan	CPU, MB, Card side Temperature Sensing Fan Tachometer		
•			
	CPU Quiet Fan (Allow Chassis Fan Speed Auto-Adjust by		
	CPU Temperature)		
	Fan Multi-Speed Control		
Voltage	PSU +12V & +5VSB & +3.3V, +3VSB, PVNN_PCH, P1V05_		
	PCH_STBY, P1V8_PCH_STBY, +BAT, PVCCIN_CPU0,		
	PVCCIN_CPU1, PVCCINFAON_CPU0, PVCCINFAON_		
	CPU1, PVCCFA_EHV_FIVRA_CPU0, PVCCFA_EHV_		
Canyon Managam	FIVRA_CPU1, PVCCD_HV_CPU0, PVCCD_HV_CPU1		
Server Manageme BMC Controller			
IPMI Dedicated	ASPEED AST2600: IPMI2.0 with iKVM and vMedia support  1 Realtek RTL8211F for dedicated management GLAN		
	1 Realter R1 L6211F for dedicated management GLAN		
GLAN			
System BIOS	AMI UEFI BIOS; 512Mb SPI Flash ROM		
Type Features			
reatures	Plug and Play, ACPI 4.0 and above compliance wake up		
Internal Connect	events, SMBIOS 3.4 and above, ASRock Rack Instant Flash		
PSU Connectors	1 (24-pin, ATX PSU signal), 4 (8-pin, ATX 12V)		
Auxiliary Panel	1 (18-pin): chassis intrusion, system fault LED, LAN1/LAN2		
Header System Panel	activity LED, locate, SMBus		
•	1 (9-pin): power switch, reset switch, system power LED,		
Header	HDD activity LED		
NMI Header COM Header			
Fan Headers	1 (9-pin)		
TPM Header	8 (6-pin) 1 (13-pin, SPI)		
RAID Header	1 (13-piii, 8+1)		
HSBP	2		
IPMB Header	1		
SMbus Headers	2		
PMbus Header	1		
Clear CMOS	1 (contact pads)		
USB Header	1 header (19-pin, 2 USB3.2 Gen1)		
Others	1 Micro SD card slot, 1 mini SAS HD connector		
LED Indicators			
Standby Power	1 (5VSB)		
LED			
80 Debug Port	1		
LED			
Fan Fail LED	8		
BMC Heartbeat	1		
LED	_		

NOTE: Please refer to the website for the latest specifications.



Temperature

This motherboard supports Wake from on Board LAN. To use this function, please make sure that the "Wake on Magic Packet from power off state" is enabled in Device Manager > Intel® Ethernet Connection > Power Management. And the "PCI Devices Power On" is enabled in UEFI SETUP UTILITY > Advanced > ACPI Configuration. After that, onboard LAN1&2 can wake up S5 under OS.

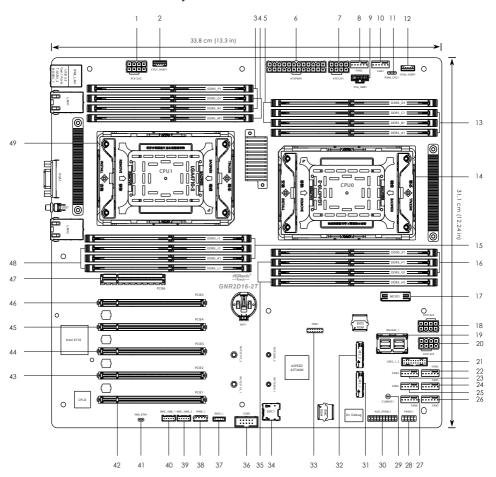


If installing Intel\* LAN utility or Marvell SATA utility, this motherboard may fail Windows\* Hardware Quality Lab (WHQL) certification tests. If installing the drivers only, it will pass the WHQL tests.

## 1.3 Unique Features

ASRock Rack Instant Flash is a BIOS flash utility embedded in Flash ROM. This convenient BIOS update tool allows user to update system BIOS without entering operating systems first like MS-DOS or Windows. With this utility, press the <F6> key during the POST or the <F2> key to enter into the BIOS setup menu to access ASRock Rack Instant Flash. Just launch this tool and save the new BIOS file to the USB flash drive, floppy disk or hard drive, then update the BIOS only in a few clicks without preparing an additional floppy diskette or other complicated flash utility. Please be noted that the USB flash drive or hard drive must use FAT32/16/12 file system.

## 1.4 Motherboard Layout



 $\Lambda$ 

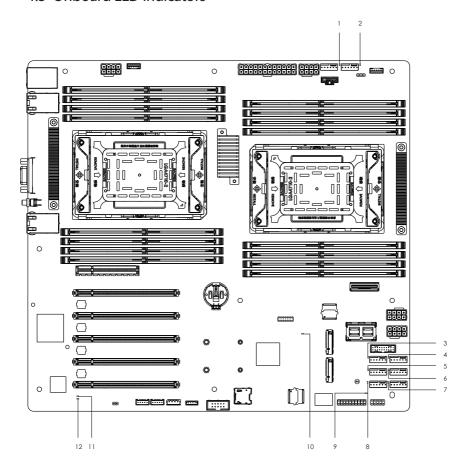
The illustration here is for references only. The actual layout may slightly vary depending on the model and the version used.

No.	Description
1	ATX 12V Power Connector (ATX12V2)
2	Backplane PCI Express Hot-Plug Connector (CPU1_HSBP1)
3	2 DDR5 DIMM Slots (DDR5_N1, DDR5_P1)*
4	2 DDR5 DIMM Slots (DDR5_M1, DDR5_O1)*
5	2 DDR5 DIMM Slots (DDR5_B1, DDR5_D1)*
6	ATX Power Connector (ATXPWR1)
7	ATX 12V Power Connector (ATX12V1)
8	System Fan Connector (FAN2)
9	PSU SMBus (PSU_SMB1)
10	System Fan Connector (FAN1)
11	PWM Configuration Header (PWM_CFG1)
12	Backplane PCI Express Hot-Plug Connector (CPU0_HSBP1)
13	2 DDR5 DIMM Slots (DDR5_A1, DDR5_C1)*
14	Socket E2 (LGA4710) (CPU0)
15	2 DDR5 DIMM Slots (DDR5_I1, DDR5_K1)*
16	2 DDR5 DIMM Slots (DDR5_E1, DDR5_G1)*
17	Mini Cool Edge IO Connector (MCIO1)
18	ATX 12V Power Connector (ATX12V3)
19	Mini-SAS HD Connector (MiniSAS_1)
20	ATX 12V Power Connector (ATX12V4)
21	USB 3.2 Gen1 Header (USB3_1_2)
22	System Fan Connector (FAN3)
23	System Fan Connector (FAN4)
24	System Fan Connector (FAN5)
25	System Fan Connector (FAN6)
26	System Fan Connector (FAN7)
27	System Fan Connector (FAN8)
28	System Panel Header (PANEL1)
29	Clear CMOS Pad (CLRMOS1)
30	Auxiliary Panel Header (AUX_PANEL1)
31	M.2 Socket (M2_1) (Type 2280 / 22110)
32	M.2 Socket (M2_2) (Type 2280 / 22110)
33	SPI TPM Header (TPM1)
34	MicroSD Card Slot (SDC1)

No.	Description
35	2 DDR5 DIMM Slots (DDR5_F1, DDR5_H1)*
36	COM Port Header (COM1)
37	Virtual RAID On CPU Header (RAID_1)
38	Intelligent Platform Management Bus Header (IPMB_1)
39	BMC SMBus Header (BMC_SMB_2)
40	BMC SMBus Header (BMC_SMB_1)
41	Non Maskable Interrupt Button (NMI_BTN1)
42	PCI Express 5.0/CXL2.0 x16 Slot (PCIE1)
43	PCI Express 5.0/CXL2.0 x16 Slot (PCIE2)
44	PCI Express 5.0/CXL2.0 x16 Slot (PCIE3)
45	PCI Express 5.0/CXL2.0 x16 Slot (PCIE4)
46	PCI Express 5.0/CXL2.0 x16 Slot (PCIE5)
47	PCI Express 5.0/CXL2.0 x8 Slot (PCIE6)
48	2 DDR5 DIMM Slots (DDR5_J1, DDR5_L1)*
49	Socket E2 (LGA4710) (CPU1)

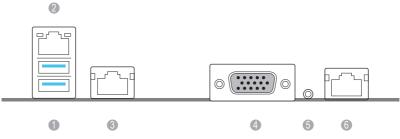
 $<sup>^*</sup>$ For DIMM installation and configuration instructions, please see p.20 (Installation of Memory Modules (DIMM)) for more details.

## 1.5 Onboard LED Indicators



No.	Item	Status	Description
1	FAN_LED2	Red	FAN2 failed
2	FAN_LED1	Red	FAN1 failed
3	FAN_LED4	Red	FAN4 failed
4	FAN_LED3	Red	FAN3 failed
5	FAN_LED6	Red	FAN6 failed
6	FAN_LED5	Red	FAN5 failed
7	FAN_LED7	Red	FAN7 failed
8	FAN_LED8	Red	FAN8 failed
9	SB_PWR1	Green	STB PWR ready
10	BMC_LED1	Green	BMC heartbeat LED
11	LED_CATERR1	Red	CPU CATERR failed
12	LED_CPLD_HB1	Green	CPLD heartbeat LED

## 1.6 I/O Panel



No.	Description	No.	Description
1	USB 3.2 Gen1 Ports (USB3_1_2)	4	VGA Port (VGA1)
2	LAN RJ-45 Port (IPMI_LAN)*	5	UID Switch (UID1)
3	10G LAN RJ-45 Port (LAN1, shared NIC)**	6	10G LAN RJ-45 Port (LAN2)**

#### **LAN Port LED Indications**

\*There is an LED on each side of IPMI LAN port. Please refer to the table below for the LAN port LED indications.



#### **IPMI LAN Port LED Indications**

Activity / Link LED		Speed LED	
Status	Description	Status	Description
Off	No Link	Off	10Mbps connection or
			no link
Blinking Yellow	Data Activity	Yellow	100Mbps connection
On	Link	Green	1Gbps connection

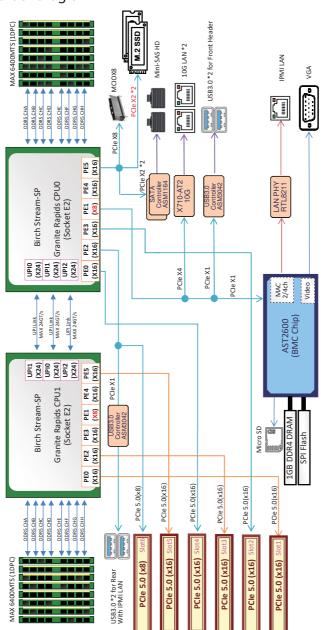
\*\*There is an LED on each side of the 10G LAN port. Please refer to the table below for the LAN port LED indications.



#### 10G LAN Port LED Indications

Activity / Link LED		Speed LED	
Status	Description	Status	Description
Off	No Link	Off	100Mbps connection or no link
Blinking Yellow	Data Activity	Yellow	1Gbps connection
On	Link	Green	10Gbps connection

## 1.7 Block Diagram



# **Chapter 2 Installation**

This is a EEB form factor (12" x13", 30.5cm x 33cm) motherboard. Before installing the motherboard, study the configuration of the chassis to ensure that the motherboard fits into it.



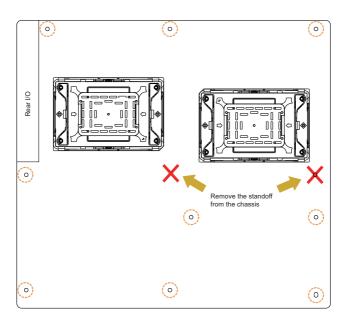
Make sure to unplug the power cord before installing or removing the motherboard. Failure to do so may cause physical injuries and motherboard damages.

## 2.1 Screw Holes

Place screws into the holes indicated by circles to secure the motherboard to the chassis.



Do not over-tighten the screws! Doing so may damage the motherboard.





Do not over-tighten the screws! Doing so may damage the motherboard.

## 2.2 Pre-installation Precautions

Take note of the following precautions before installing motherboard components or change any motherboard settings.

- 1. Unplug the power cord from the wall socket before touching any components.
- To avoid damaging the motherboard's components due to static electricity, NEVER place the motherboard directly on the carpet or the like. Also remember to use a grounded wrist strap or touch a safety grounded object before handling the components.
- 3. Hold components by the edges and do not touch the ICs.
- Whenever uninstall any component, place it on a grounded anti-static pad or in the bag that comes with the component.
- 5. When placing screws into the screw holes to secure the motherboard to the chassis, please do not over-tighten the screws! Doing so may damage the motherboard.



Before installing or removing any component, ensure that the power is switched off or the power cord is detached from the power supply. Failure to do so may cause server damage to the motherboard, peripherals, and/or components.

# 2.3 Installing the CPU and Heatsink

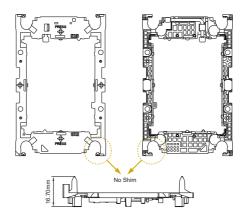


- 1. Unplug all power cables before installing the CPU.
- 2. Illustration in this documentation are examples only.

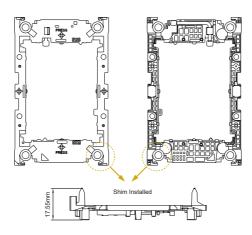
#### Carrier Used

Carrier Type	Xeon® SP XCC	Xeon® SP MCC/LCC
Carrier Code	E2A	E2B
Shim	No	Yes
Carrier Height	16.70mm	17.55mm

#### **XCC Carrier**



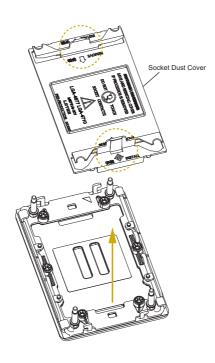
#### **MCC Carrier**

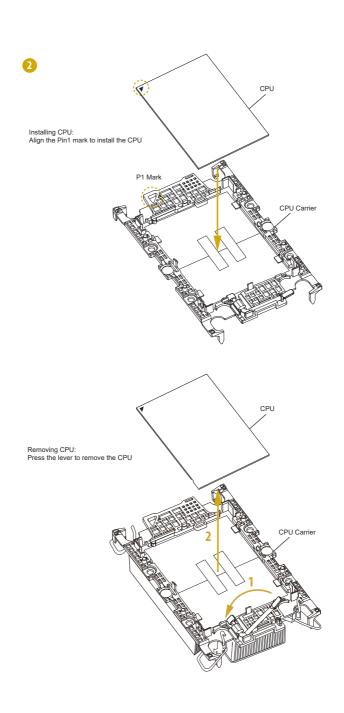




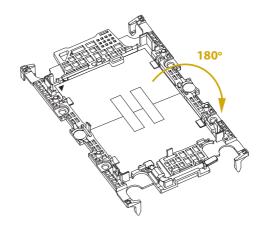
- Before inserting the CPU into the socket, please check if the PnP cap is on the socket, if the CPU surface is unclean, or if there are any bent pins in the socket. Do not force to insert the CPU into the socket if above situation is found. Otherwise, the CPU will be seriously damaged.
- 2. Unplug all power cables before installing the CPU.



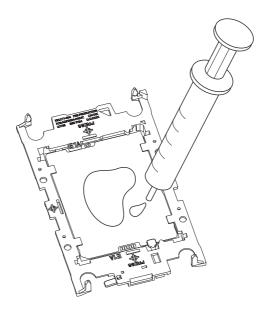




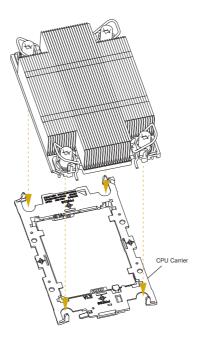




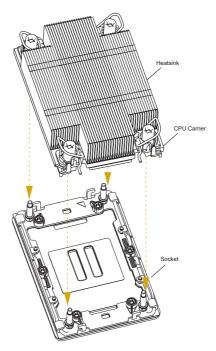




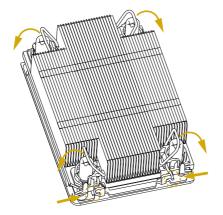




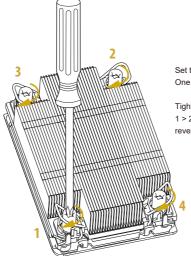












Set the torque wrench to 6-12 in-lb. One fourth a turn each time.

Tighten the screws in a sequential order 1 > 2 > 3 > 4. Loosen the screws in a reverse order.

## 2.4 Installation of Memory Modules (DIMM)

This motherboard provides sixteen DDR5 (Double Data Rate 5) DIMM slots in two groups, and supports Single Channel Memory Technology.

CPU0 CPU1

DDR5\_A1, B1, C1, D1, E1, F1, G1 H1 DDR5\_I1, J1, K1, L1, M1, N1, O1, P1



- 1. Before installing a memory module, make sure to turn off the computer and unplug the powercord from the power outlet to prevent damage to the memory module.
- 2. It is not allowed to install a DDR, DDR2, DDR3 or DDR4 memory module into a DDR5 slot; otherwise, this motherboard and DIMM may be damaged.
- 3. For sixteen channel configurations, it needs to install identical (the same brand, speed, size and chip-type) DDR5 DIMMs.

## **Recommended Memory Configurations**

## 1 CPU Configurations (Single P)

CPU0								
DDR5	A1	B1	<b>C</b> 1	D1	E1	F1	G1	H1
1 DIMM	V							
4 DIMMS	V		V		V		V	
4 DIMMS		V		V		V		V
8 DIMMS	V	V	V	V	V	V	V	V

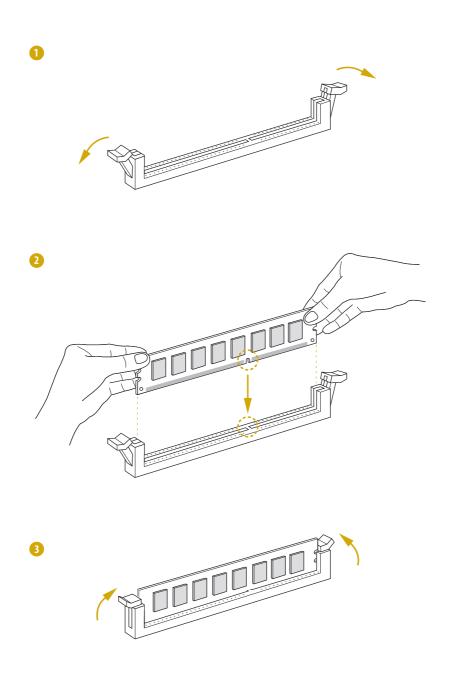
The symbol V indicates the slot is populated.

## 2 CPU Configurations (Dual P)

CPU0								
DDR5	<b>A</b> 1	B1	<b>C</b> 1	D1	E1	F1	G1	H1
2 DIMMS	V							
8 DIMMS	V		V		V		V	
8 DIMMS		V		V		V		V
16 DIMMS	V	V	V	V	V	V	V	V

CPU1								
DDR5	l1	J1	K1	L1	M1	N1	01	P1
2 DIMMS	V							
8 DIMMS	V		V		V		V	
8 DIMMS		V		V		V		V
16 DIMMS	V	V	V	V	V	V	V	V

The symbol V indicates the slot is populated.



### 2.5 Expansion Slots (PCI Express Slots)

There are 6 PCI Express slots on this motherboard.

#### PCIF slots:

PCIE6 (PCIE 5.0 x8 slot, from CPU0) is used for PCI Express x8 lane width cards. PCIE5 (PCIE 5.0 x16 slot, from CPU1) is used for PCI Express x16 lane width cards. PCIE4 (PCIE 5.0 x16 slot, from CPU0) is used for PCI Express x16 lane width cards. PCIE3 (PCIE 5.0 x16 slot, from CPU1) is used for PCI Express x16 lane width cards. PCIE2 (PCIE 5.0 x16 slot, from CPU0) is used for PCI Express x16 lane width cards. PCIE1 (PCIE 5.0 x16 slot, from CPU1) is used for PCI Express x16 lane width cards.

Slot	Generation	Mechanical	Electrical	Source
PCIE6	5.0	x8	x8	CPU0
PCIE5	5.0	x16	x16	CPU1
PCIE4	5.0	x16	x16	CPU0
PCIE3	5.0	x16	x16	CPU1
PCIE2	5.0	x16	x16	CPU0
PCIE1	5.0	x16	x16	CPU1

#### Installing an expansion card

- Step 1. Before installing an expansion card, please make sure that the power supply is switched off or the power cord is unplugged. Please read the documentation of the expansion card and make necessary hardware settings for the card before starting the installation.
- Step 2. Remove the system unit cover (if the motherboard is already installed in a chassis).
- Step 3. Remove the bracket facing the slot that intending to use on the chassis. Keep the screw for later use.
- Step 4. Install the PCIE card to the riser card.
- Step 5. Align the riser-card assembly connector with the slot on the system board.

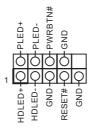
  Press firmly until the riser-card assembly is completely seated on the slot.
- Step 6. Fasten the card to the chassis with the screw.
- Step 7. Replace the system cover.

#### 2.6 Onboard Headers and Connectors



Onboard headers and connectors are NOT jumpers. Do NOT place jumper caps over these headers and connectors. Placing jumper caps over the headers and connectors will cause permanent damage to the motherboard.

System Panel Header (9-pin PANEL1) (see p.6, No.28)



Connect the power switch, reset switch and system status indicator on the chassis to this header according to the pin assignments. Particularly note the positive and negative pins before connecting the cables.



#### PWRBTN (Power Switch):

Connect to the power switch on the chassis front panel and configure the way to turn off the system using the power switch.

#### RESET (Reset Switch):

Connect to the reset switch on the chassis front panel. Press the reset switch to restart the computer if the computer freezes and fails to perform a normal restart.

#### PLED (System Power LED):

Connect to the power status indicator on the chassis front panel. The LED is on when the system is operating. The LED is off when the system is in S4 sleep state or powered off (S5).

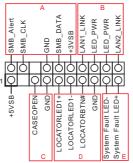
#### HDLED (Hard Drive Activity LED):

Connect to the hard drive activity LED on the chassis front panel. The LED is on when the hard drive is reading or writing data.

The front panel design may differ by chassis. A front panel module mainly consists of power switch, reset switch, power LED, hard drive activity LED, speaker and etc. When connecting the chassis front panel module to this header, make sure the wire assignments and the pin assignments are matched correctly.

Enalish

Auxiliary Panel Header (18-pin AUX PANEL1) (see p.6, No.30)



This header supports multiple functions on the front panel, including the front panel SMB, internet status indicator and chassis intrusion pin.



#### A. Front panel SMBus connecting pin (6-1 pin FPSMB):

This header allows user to connect SMBus (System Management Bus) equipment. It can be used for communication between peripheral equipment in the system, which has slower transmission rates, and power management equipment.

#### B. Internet status indicator (4-pin LAN, LED):

This header allows user to use the Gigabit internet indicator cable to connect to the LAN status indicator. When this indicator flickers, it means that the internet is properly connected.

#### C. Chassis intrusion pin (2-pin CHASSIS):

This header is provided for host computer chassis with chassis intrusion detection designs. In addition, it must also work with external detection equipment, such as a chassis intrusion detection sensor or a microswitch. When this function is activated, if any chassis component movement occurs, the sensor will immediately detect it and send a signal to this header, and the system will then record this chassis intrusion event. The default setting is set to the CASEOPEN and GND pin; this function is off.

#### D. Locator LED (4-pin LOCATOR):

This header is for the locator switch and LED on the front panel.

#### E. System Fault LED (2-pin SYSTEM LED):

This header is for the Fault LED on the system.

Non Maskable Interrupt Button Header (NMI\_BTN1) (see p.6, No.41)



Please connect a NMI device to this header.

PWM Configuration Header (3-pin PWM\_CFG1) (see p.6, No.11)



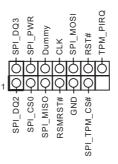
This header is used for PWM configurations.

USB 3.2 Gen1 Header (19-pin USB3\_1\_2) (see p.6, No.21)



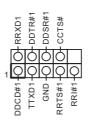
Besides two default USB 3.2 Gen1 ports on the I/O panel, there is one USB 3.2 Gen1 header on this motherboard. This USB 3.2 Gen1 header can support two USB 3.2 Gen1 ports.

SPI TPM Header (13-pin TPM1) (see p.6, No.33)



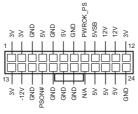
This connector supports SPI Trusted Platform Module (TPM) system, which can securely store keys, digital certificates, passwords, and data. A TPM system also helps enhance network security, protects digital identities, and ensures platform integrity.

COM Port Header (9-pin COM1) (see p.6, No.36)



This COM1 header supports a serial port module.

ATX Power Connector (24-pin ATXPWR1) (see p.6, No.6)



This motherboard provides a 24-pin ATX power connector. To use a 20-pin ATX power supply, please plug it along Pin 1 and Pin 13.

Enalish

ATX 12V Power

Connectors

(8-pin ATX12V1)

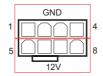
(see p.6, No.7)

(8-pin ATX12V2)

(see p.6, No.1)

(8-pin ATX12V3)

(see p.6, No.18)



This motherboard provides four 8-pin ATX 12V power connectors.





(8-pin ATX12V4 (see p.6, No.20)



(6-pin FAN1)

(see p.6, No.10)

(6-pin FAN2)

(see p.6, No.8)

(6-pin FAN3)

(see p.6, No.22)

(6-pin FAN4)

(see p.6, No.23)

(6-pin FAN5)

(see p.6, No.24)

(6-pin FAN6)

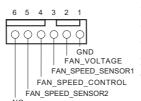
(see p.6, No.25)

(6-pin FAN7)

(see p.6, No.26)

(6-pin FAN8)

(see p.6, No.27)



Please connect fan cables to the fan connectors and match the black wire to the ground pin. All fans support Fan Control.

MicroSD Card Slot (SDC1) (see p.6, No.34)



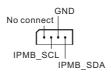
Carefully insert the SD Card into the slot until it clicks.

PSU SMBus (PSU\_SMB1) (see p.6, No.9)



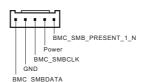
PSU SMBus monitors the status of the power supply, fan and system temperature.

Intelligent Platform Management Bus header (4-pin IPMB\_1) (see p.6, No.38)



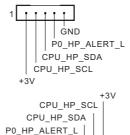
This 4-pin connector is used to provide a cabled base-board or front panel connection for value added features and 3rd-party add-in cards, such as Emergency Management cards, that provide management features using the IPMB.

BMC SMBus Headers (5-pin BMC\_SMB\_1) (see p.6, No.40) (5-pin BMC\_SMB\_2) (see p.6, No.39)



These headers are used for the SM BUS devices.

Backplane PCI Express Hot-Plug Connector (5-pin CPU0\_HSBP1) (see p.6, No.12) (5-pin CPU1\_HSBP1) (see p.6, No.2)



GND

This header is used for the hot plug feature of HDDs on the backplane.

Clear CMOS Pad (CLRMOS1) (see p.6, No.29)



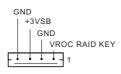
CLRMOS1 allows user to clear the data in CMOS. To clear CMOS, take out the CMOS battery and short the Clear CMOS Pad.

Mini-SAS HD Connector (MiniSAS\_1) (see p.6, No.19)



The connector supports MiniSAS-to-SATA data cables for internal storage devices with up to 6.0 Gb/s data transfer rate.

Virtual RAID On CPU Header (4-pin RAID\_1) (see p.6, No.37)



This connector supports Intel® Virtual RAID on CPU and NVME/AHCI RAID on CPU PCIE.

With the introduction of the Intel VROC product, there are three modes of operation:

SKU	HW key required	Key features
Pass-thru	Not needed	<ul> <li>Pass-thru only (no RAID)</li> <li>LED Management</li> <li>Hot Plug Support</li> <li>RAID 0 support for Intel Fultondale NVMe SSDs</li> </ul>
Standard	VROCSTANMOD	<ul><li>Pass-thru SKU features</li><li>RAID 0, 1, 10</li></ul>
Premium	VROCPREMMOD	<ul> <li>Standard SKU features</li> <li>RAID 5</li> </ul>
ISS	VROCISSDMOD	• RAID 5 Write Hole Closure

<sup>\*</sup>Only Intel SSDs are supported.

MCIO Connector (MCIO1) (see p.6, No.17)



The connector is used for the PCIE device.

<sup>\*</sup>For further details on VROC, please refer to the official information released by Intel.

## MCIO1 Pin Definition [CPU0]

Pin	Defeinition	Pin	Defeinition
A1	GND	B1	GND
A2	PE5_RX_DP15	B2	PE5_TX_C_DN15
A3	PE5_RX_DN15	В3	PE5_TX_C_DP15
A4	GND	B4	GND
A5	PE5_RX_DP14	B5	PE5_TX_C_DP14
A6	PE5_RX_DN14	В6	PE5_TX_C_DN14
A7	GND	В7	GND
A8	MCIO1_BP_TYPE	B8	MCIO1_SCL1
A9	MCIO1_2WIRE_RST_N	В9	MCIO1_SDA1
A10	GND	B10	GND
A11	PCIE_GEN5_MCIO1A_CLKP	B11	MCIO1_PERST_BUF1_N
A12	PCIE_GEN5_MCIO1A_CLKN	B12	MCIO1_PRSNT1_N
A13	GND	B13	GND
A14	PE5_RX_DP13	B14	PE5_TX_C_DP13
A15	PE5_RX_DN13	B15	PE5_TX_C_DN13
A16	GND	B16	GND
A17	PE5_RX_DP12	B17	PE5_TX_C_DP12
A18	PE5_RX_DN12	B18	PE5_TX_C_DN12
A19	GND	B19	GND
A20	PE5_RX_DP11	B20	PE5_TX_C_DP11
A21	PE5_RX_DN11	B21	PE5_TX_C_DN11
A22	GND	B22	GND
A23	PE5_RX_DN10	B23	PE5_TX_C_DP10
A24	PE5_RX_DP10	B24	PE5_TX_C_DN10
A25	GND	B25	GND
A26	-	B26	MCIO1_SCL2
A27	-	B27	MCIO1_SDA2
A28	GND	B28	GND
A29	PCIE_GEN5_MCIO1B_CLKP	B29	MCIO1_PERST_BUF2_N
A30	PCIE_GEN5_MCIO1B_CLKN	B30	MCIO1_PRSNT2_N
A31	GND	B31	GND
A32	PE5_RX_DP9	B32	PE5_TX_C_DP9
A33	PE5_RX_DN9	B33	PE5_TX_C_DN9
A34	GND	B34	GND
A35	PE5_RX_DN8	B35	PE5_TX_C_DP8
A36	PE5_RX_DP8	B36	PE5_TX_C_DN8
A37	GND	B37	GND
75	NP_NC_1	76	NP_NC_2
77	PGND_1	78	PGND_3
79	PGND_2	80	PGND_4

## 2.7 Unit Identification purpose LED/Switch

The UID button allows user to locate the server working on from behind a rack of servers.

Unit Identification purpose LED/Switch (UID1)



When the UID button on the front or rear panel is pressed, the front/rear UID blue LED indicator will be truned on. Press the UID button again to turn off the indicator.



- $1.\ Press\ and\ hold\ the\ UID\ button\ for\ 4\ seconds,\ BMC\ will\ trigger\ an\ external\ reset.$
- $2.\ Press\ and\ hold\ the\ UID\ button\ for\ 10\ seconds,\ BMC\ will\ reset\ and\ load\ default\ values.$

## 2.8 Dr. Debug

Dr. Debug is used to provide code information, which makes trouble shooting even easier. Please see the diagrams below for reading the Dr. Debug codes.

Code	Description
0x10	PEI_CORE_STARTED
0x11	PEI_CAR_CPU_INIT
0x15	PEI_CAR_NB_INIT
0x19	PEI_CAR_SB_INIT
0x31	PEI_MEMORY_INSTALLED
0x32	PEI_CPU_INIT
0x33	PEI_CPU_CACHE_INIT
0x34	PEI_CPU_AP_INIT
0x35	PEI_CPU_BSP_SELECT
0x36	PEI_CPU_SMM_INIT
0x37	PEI_MEM_NB_INIT
0x3B	PEI_MEM_SB_INIT
0x4F	PEI_DXE_IPL_STARTED
0x60	DXE_CORE_STARTED
0x61	DXE_NVRAM_INIT
0x62	DXE_SBRUN_INIT

0x63	DXE_CPU_INIT
0x68	DXE_NB_HB_INIT
0x69	DXE_NB_INIT
0x6A	DXE_NB_SMM_INIT
0x70	DXE_SB_INIT
0x71	DXE_SB_SMM_INIT
0x72	DXE_SB_DEVICES_INIT
0x78	DXE_ACPI_INIT
0x79	DXE_CSM_INIT
0x90	DXE_BDS_STARTED
0x91	DXE_BDS_CONNECT_DRIVERS
0x92	DXE_PCI_BUS_BEGIN
0x93	DXE_PCI_BUS_HPC_INIT
0x94	DXE_PCI_BUS_ENUM
0x95	DXE_PCI_BUS_REQUEST_RESOURCES
0x96	DXE_PCI_BUS_ASSIGN_RESOURCES
0x97	DXE_CON_OUT_CONNECT
0x98	DXE_CON_IN_CONNECT

0x99	DXE_SIO_INIT
0x9A	DXE_USB_BEGIN
0x9B	DXE_USB_RESET
0x9C	DXE_USB_DETECT
0x9D	DXE_USB_ENABLE
0xA0	DXE_IDE_BEGIN
0xA1	DXE_IDE_RESET
0xA2	DXE_IDE_DETECT
0xA3	DXE_IDE_ENABLE
0xA4	DXE_SCSI_BEGIN
0xA5	DXE_SCSI_RESET
0xA6	DXE_SCSI_DETECT
0xA7	DXE_SCSI_ENABLE
0xA8	DXE_SETUP_VERIFYING_PASSWORD
0xA9	DXE_SETUP_START
0xAB	DXE_SETUP_INPUT_WAIT
0xAD	DXE_READY_TO_BOOT
0xAE	DXE_LEGACY_BOOT

0xAF	DXE_EXIT_BOOT_SERVICES
0xB0	RT_SET_VIRTUAL_ADDRESS_MAP_BEGIN
0xB1	RT_SET_VIRTUAL_ADDRESS_MAP_END
0xB2	DXE_LEGACY_OPROM_INIT
0xB3	DXE_RESET_SYSTEM
0xB4	DXE_USB_HOTPLUG
0xB5	DXE_PCI_BUS_HOTPLUG
0xB6	DXE_NVRAM_CLEANUP
0xB7	DXE_CONFIGURATION_RESET
0xF0	PEI_RECOVERY_AUTO
0xF1	PEI_RECOVERY_USER
0xF2	PEI_RECOVERY_STARTED
0xF3	PEI_RECOVERY_CAPSULE_FOUND
0xF4	PEI_RECOVERY_CAPSULE_LOADED
0xE0	PEI_S3_STARTED
0xE1	PEI_S3_BOOT_SCRIPT
0xE2	PEI_S3_VIDEO_REPOST

0xE3	PEI_S3_OS_WAKE
0x50	PEI_MEMORY_INVALID_TYPE
0x53	PEI_MEMORY_NOT_DETECTED
0x55	PEI_MEMORY_NOT_INSTALLED
0x57	PEI_CPU_MISMATCH
0x58	PEI_CPU_SELF_TEST_FAILED
0x59	PEI_CPU_NO_MICROCODE
0x5A	PEI_CPU_ERROR
0x5B	PEI_RESET_NOT_AVAILABLE
0xD0	DXE_CPU_ERROR
0xD1	DXE_NB_ERROR
0xD2	DXE_SB_ERROR
0xD3	DXE_ARCH_PROTOCOL_NOT_AVAILABLE
0xD4	DXE_PCI_BUS_OUT_OF_RESOURCES
0xD5	DXE_LEGACY_OPROM_NO_SPACE
0xD6	DXE_NO_CON_OUT
0xD7	DXE_NO_CON_IN

0xD8	DXE_INVALID_PASSWORD
0xD9	DXE_BOOT_OPTION_LOAD_ERROR
0xDA	DXE_BOOT_OPTION_FAILED
0xDB	DXE_FLASH_UPDATE_FAILED
0xDC	DXE_RESET_NOT_AVAILABLE
0xE8	PEI_MEMORY_S3_RESUME_FAILED
0xE9	PEI_S3_RESUME_PPI_NOT_FOUND
0xEA	PEI_S3_BOOT_SCRIPT_ERROR
0xEB	PEI_S3_OS_WAKE_ERROR

## 2.9 Dual LAN and Teaming Operation Guide

Dual LAN with Teaming enabled on this motherboard allows two single connections to act as one single connection(s) for twice the transmission bandwidth, making data transmission more effective and improving the quality of transmission of distant images. Fault tolerance on the dual LAN network prevents network downtime by transferring the workload from a failed port to a working port.



The speed of transmission is subject to the actual network environment or status even with Teaming enabled.

Before setting up Teaming, please make sure whether the Switch (or Router) supports Teaming (IEEE 802.3ad Link Aggregation). Specify a preferred adapter in Intel PROSet. Under normal conditions, the Primary adapter handles all non-TCP/IP traffic. The Secondary adapter will receive fallback traffic if the primary fails. If the Preferred Primary adapter fails, but is later restored to an active status, control is automatically switched back to the Preferred Primary adapter.

#### Step 1

From Device Manager, open the properties of a team.

#### Step 2

Click the **Settings** tab.

#### Step 3

Click the Modify Team button.

#### Step 4

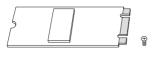
Select the adapter to the primary adapter and click the **Set Primary** button.

If it does not specify a preferred primary adapter, the software will choose an adapter of the highest capability (model and speed) to act as the default primary. If a failover occurs, another adapter becomes the primary. The adapter will, however, rejoin the team as a non-primary.

## 2.10 M.2 SSD Module Installation Guide

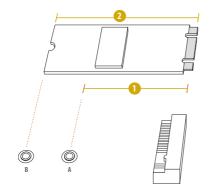
The M.2 Socket (M2\_1/M2\_2, Key M) supports type 2280/22110 M.2 PCI Express module up to Gen5 x2 (32GT/s x2).

## Installing the M.2\_SSD Module



### Step 1

Prepare a M.2 SSD module and the screw.



### Step 2

Depending on the PCB type and length of the M.2 SSD module, find the corresponding nut location to be used.

No.	1	2
Nut Location	A (NUT2280_1/NUT2280_2)	B (NUT22110_1/NUT22110_2)
PCB Length	8cm	11cm
Module Type	Type 2280	Type 22110



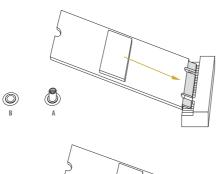
## Step 3

Move the standoff based on the module type and length.
Skip Step 3 and 4 and go straight to Step 5 if using the default nut.
Otherwise, release the standoff by hand.



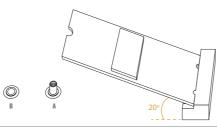
## Step 4

Peel off the yellow protective film on the nut to be used. Hand tighten the standoff into the desired nut location on the motherboard.



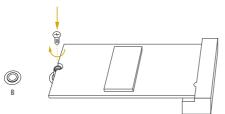
#### Step 5

Align and gently insert the M.2 SSD module into the M.2 slot. Please be aware that the M.2 SSD module only fits in one orientation.



### Step 6

Tighten the screw with a screwdriver to secure the module into place.
Please do not overtighten the screw as this might damage the module.



## **Chapter 3 UEFI Setup Utility**

### 3.1 Introduction

This section explains how to use the UEFI SETUP UTILITY to configure the system. The UEFI chip on the motherboard stores the UEFI SETUP UTILITY. Run the UEFI SETUP UTILITY when starting up the computer. Please press <F2> or <Del> during the Power-On-Self-Test (POST) to enter the UEFI SETUP UTILITY; otherwise, POST will continue with its test routines.

Restart the system by pressing <Ctrl> + <Alt> + <Delete> to enter the UEFI SETUP UTI ITY after POST, or by pressing the reset button on the system chassis. This allows user to restart by turning the system off and then back on.



Because the UEFI software is constantly being updated, the following UEFI setup screens and descriptions are for reference purpose only, and they may not exactly match what seeing on the screen.

#### 3.1.1 UFFI Menu Bar

The top of the screen has a menu bar with the following selections:

ltem	Description
Main	To set up the system time/date information
Advanced	To set up the advanced UEFI features
Server Mgmt	To manage the server
Event Logs	For event log configuration
Security	To set up the security features
Boot	To set up the default system device to locate and load the Operating System
Exit	To exit the current screen or the UEFI SETUP UTILITY

Use <  $\leftarrow$  > key or <  $\longrightarrow$  > key to choose among the selections on the menu bar, and then press <Enter> to get into the sub screen.

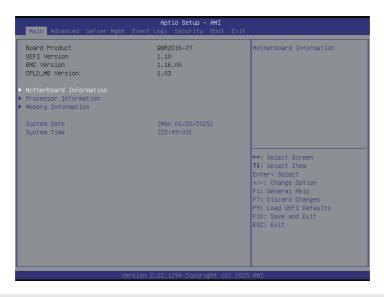
## 3.1.2 Navigation Keys

Please check the following table for the function description of each navigation key.

Navigation Key(s)	Function Description
<b>←</b> / <b>→</b>	Moves cursor left or right to select Screens
<b>↑</b> / <b>↓</b>	Moves cursor up or down to select items
+ / -	To change option for the selected items
<tab></tab>	Switch to next function
<enter></enter>	To bring up the selected screen
<pgup></pgup>	Go to the previous page
<pgdn></pgdn>	Go to the next page
<home></home>	Go to the top of the screen
<end></end>	Go to the bottom of the screen
<f1></f1>	To display the General Help Screen
<f7></f7>	Discard changes and exit the UEFI SETUP UTILITY
<f9></f9>	Load optimal default values for all the settings
<f10></f10>	Save changes and exit the UEFI SETUP UTILITY
<f12></f12>	Print screen
<esc></esc>	Jump to the Exit Screen or exit the current screen

## 3.2 Main Screen

Once entering the UEFI SETUP UTILITY, the Main screen will appear and display the system overview. The Main screen provides system overview information and allows user-to set the system time and date.





The screenshots in this user manual are examples and for references only. The actual images may slightly vary depending on the model and the version used.

### 3.2.1 Mother Board Information

Press <Enter> to view the information of the motheboard.



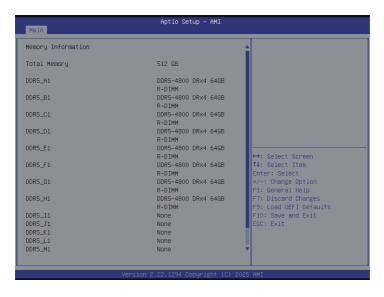
### 3.2.2 Processor Information

Press <Enter> to view the information of the processor.



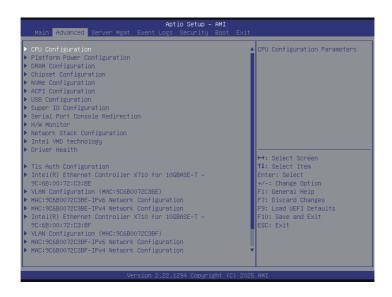
## 3.2.3 Memory Information

Press <Enter> to view the information of the memory.



## 3.3 Advanced Screen

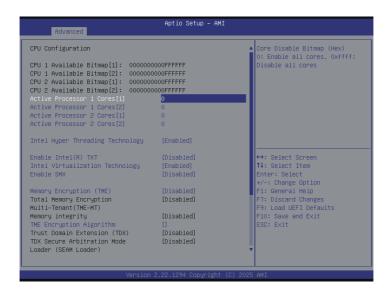
In this section, set the configurations for the following items: CPU Configuration, Platform Power Configuration, DRAM Configuration, Chipset Configuration, NVMe Configuration, ACPI Configuration, USB Configuration, Super IO Configuration, Serial Port Console Redirection, H/W Monitor, Network StackConfiguration, Intel VMD Technology, Driver Health, Tls Auth Configuration, Intel(R) Ethernet Controller, VLAN Configuration and Instant Flash.





Setting wrong values in this section may cause the system to malfunction.

## 3.3.1 CPU Configuration



## Active Processor 1 Cores [1]/[2]

Core Disable Bitmap (Hex)

0: Enable all cores.

0xffff: Didable all cores.

## Active Processor 2 Cores [1]/[2]

Core Disable Bitmap (Hex)

0: Enable all cores.

0xffff: Didable all cores.

## Intel Hyper Threading Technology

Intel Hyper Threading Technology allows multiple threads to run on each core, so that the overall performance on threaded software is improved.

## Enable Intel(R) TXT

Enables Intel Trusted Execution Technology Configuration.

## Intel Virtualization Technology

Intel Virtualization Technology allows a platform to run multiple operating systems and applications in independent partitions, so that one computer system can function as multiple virtual systems.

#### **Enable SMX**

Use this item to enable Safer Mode Extensions.

### Memory Encryption (TME)

Use this item to enable or disable Memory Encryption (TME).

## Total Memory Encryption Multi-Tenant (TME-MT)

Use this item to enable or disable Total Memory Encryption-Multi-Tenant (TME-MT). Can be enabled only if Socket Configuration -> Porcessor Configuration -> Limit CPU PA to 46 bits=Disable.

## Memory Integrity

Use this item to enable or disable memory Integrity.

## TME Encryption Algorithm

Use this item to select TME Encryption Algorithm.

### Trust Domain Extension (TDX)

Use this item to enable or disable Trust Domain Extension (TDX).

## TDX Secure Arbitration Mode Loader (SEAM Loader)

Use this item to enable or disable TDX Secure Arbitration Mode Loader (SEAM Loader).

## **SGX Factory Reset**

Perform SGX Factory Reset, on subsequent boot: delete all registration data, if SGX enabled will force Initial Platform Establishment flow.

### SW Guard Extensions (SGX)

Use this item to enable or disable Software Guard Extensions (SGX).

### SGX PRMRR Size Requested

Use this item to select the SGX PRMRR Size per domain.

#### SGX OoS

Use this item to enable or disable SGX Quality of Service.

## Select Owner EPOCH input type

Each EPOCH is 64bit, keys value should be different than 0. After generating new epoch via 'Change to New Random Owner EPOCHs', the selection reverts back to 'Manual User Defined Owner EPOCHs'. In next boot keys will be hidden and state will be 'SGX Owner EPOCH activated'.

### DCU Streamer Prefetcher

DCU streamer prefetcher is an L1 data cache prefetcher (MSR 1A4h [2])

#### Hardware Prefetcher

Automatically prefetch data and code for the processor. Enable for better performance.

## Adjacent Cache Line Prefetch

Automatically prefetch the subsequent cache line while retrieving the currently requested cache line. Enable for better performance.

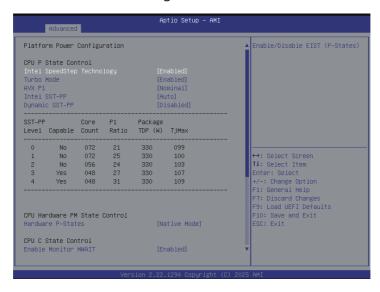
#### AFS-NI

Use this item to enable or disable AES-NI support.

#### SNC

Use this item to enable or disable SNC mode. Configure this item to Disable, BIOS will enable UMA mode. Number of clusters is determined by BIOS based on detected SKU.

## 3.3.2 Platform Power Configuration



## Intel SpeedStep Technology

Intel SpeedStep technology allows processors to switch between multiple frequencies and voltage points for better power saving and heat dissipation. CPU turbo ratio can be fixed when Intel SpeedStep Technology set Disabled and Intel Turbo Boost Technology set Enabled.



Please note that enabling this function may reduce CPU voltage and lead to system stability or compatibility issues with some power supplies. Please set this item to [Disabled] if above issues occur.

#### Turbo Mode

Use this item to enable the processor to run above its base operating frequency when the operating system requests the highest performance state.

#### AVX P1

Use this item to select AVX P1 level.

#### Intel SST-PP

Intel SST-PP Select allows user to choose level. Auto: Choose the lowest level hardware support.

## Dynamic SST-PP

Use this item to enable or disable the Dynamic SST-PP.



HWP Native Mode is a pre-requisite for enabling Dynamic SST-PP.

#### Hardware P-States

This item supports below selections:

Disable: Hardware chooses a P-state based on OS Request (Legacy P-States).

Native Mode: Hardware chooses a P-state based on OS guidance.

Out of Band Mode: Hardware autonomously chooses a P-state (no OS guidance)

**Native Mode with No Legacy Support:** Hardware autonomously chooses a P-state based on OS guidance with no legacy support.

#### **Fnable Monitor MWAIT**

Use this item to configure Monitor and MWAIT instructions whether Auto maps to enable.

#### **ACPI C6x Enumeration**

Auto: Maps to C6-P as ACPI C2.

Disable: Don't enumerate any C6 state in ACPI

C6 as ACPI C2/C3: Enumerate C6 as ACPI C2/C3 state. PkgC6 is not allowed.

C6-P as ACPI C2/C3: Enumerate C6-P as ACPI C2/C3 state. PkgC6 is allowed.

#### C1 to Cle Promotion

CPU will promote C1 request to Cle state.

### Package C State

Package C State limit, the state Auto maps is program specific.

### **Power Performance Tuning**

Use this item to decides which controls EFB.

OS Controls EPB: Specifies IA32\_ENERGY\_PERF\_BIAS is used.

BIOS Controls EPB: Specifies ENERGY\_PERF\_BIAS\_CONFIG is used.

PECI Controls EPB: Specifies PCS53 is used.

#### ENERGY PERF BIAS CFG mode

Use this item to use input from ENERGY\_PERF\_BIAS\_CONFIG mode seletion. PERF/Balanced, Perf/Bananced or Power/Power.

#### PI 1 Power Limit

Use this item to configure Package Power Limit 1 (PL1) in watts. PL1 Power Limit is in Watts and the value may vary from 0 to Fused Value. If the value is 0, the fused value will be programmed. A value greater than fused TDP value will not be programmed.

### PL1 Time Window

PL1 value in seconds. The value may vary from 1 to 5. Indicates the time window over which TDP value should be maintained.

#### PL2 Power Limit

PL2 Power Limit in Watts. The value may vary from 0 to 120% Fused TDP Value. If the value is 0, BIOS programs 120% \* Fused TDP value.

### PL2 Time Window

PL2 value in seconds. The value may vary from 0.012 to 0.039. Indicates the time window over which TDP value should be maintained.

## 3.3.3 DRAM Configuration



## **Enforce DDR Memory Frequency POR**

Select this time to enable or disable the Enforces Plan of Record restrictions for DDR frequency programming.

## **DRAM Frequency**

Maximum Host DDR Memory Frequency Selections in MT/s. If the "AUTO" option has been selected, a frequency is chosen automatically based on the minimum tCK given by the SPD. If Enforce POR is disabled, user will be able to run at higher frequencies than the memory support (limited by processor support).

#### Numa

Use this item to enable or disable Non Uniform Memory Access (NUMA).

#### Mirror Mode

Full Mirror Mode will set entire 1LM memory in system to be mirrored, consequently reducing the memory capacity by half. Partial Mirror Mode will enable the required size of memory to be mirrored. If rank sparing is enabled partial mirroring will not take effect. Enabling any type of Mirror Mode will disable XPT Prefetch.

#### **UFFI ARM Mirror**

Imitate behavior of UEFI based Address Range Mirror with setup option.

### Mirror TAD0

Enable Mirror on entire memory for TAD0.

## **ARM Mirror Percentage**

Enter the percentage to be mirrored in basis points. Mirror options are 10%, 15%, 20%, 25%, 30%, 35%, 40%. 25% is represented 2500. Any other number entered will be rounded to nearest greater number, for ex. number > 2000 and <=2500 will be rounded to 2500. And any number >4000, will be rounded to 4000.

## **ADDDC Sparing**

Enable or disable Memory Rank Sparing.

## Global Scrambling

Enable - Enables global data scrambling.

Disable - Disables this feature.

## Intel(R) Flat Memory Mode Support

Enable or disable Intel(R) Flat Memory Mode support.

## DDR CXL Heterogeneous Interleave support

Use this item to enable DDR CXL Heterogeneous Interleave support.

## **CXL Noncompliant Device Support**

Enables DDR CXL Heterogeneous Interleave support.

## 3.3.4 Chipset Configuration



#### MMCFG Base

Use this item to select MMCFG Base. Auto-Auto decides based on Si compatibility.

#### MMCFG Size

Use this item to select MMCFG Size. Auto-Auto decides based on Si compatibility.

## MMIO High Base

Use this item to select MMIO High Base.

## MMIO High Granularity Size

Selects the allocation size used to assign mmioh resources. Per stack mmioh resource assignments are multiples of the granularity where 1 unit per stack is the default allocation.

## **SR-IOV Support**

If system has SR-IOV capable PCIe Devices, this option Enables or Disables Single Root IO Virtualization Support.

## Re-Sized BAR Support

If system has Resizable BAR capable PCIe Devices, this option Enables or Disables Resizable BAR Support.

#### Onboard VGA

Use this to enable or disable the Onboard VGA function.

#### Onboard I AN

Use this item to enable or disable the Boot From Onboard LAN feature.

#### PCIF Link Width

This displays the PCIE Link Width.

### PCIE1/PCIE2/PCIE3/PCIE4/PCIE5/PCIE5, MCIO1 Width

The default value is [Auto] - x16.

#### PCIE Link Speed

This displays the PCIE Link Speed.

#### PCIE1/PCIE2/PCIE3/PCIE4/PCIE6 Link Speed

The default value is [Auto] - up to Gen5 (32GT/s).

### MCIO1\_1/MCIO1\_2/M2\_1/M2\_2 Link Speed

The default value is [Auto] - up to Gen5 (32GT/s).

## **PCIE Hot Plug**

Select this item to configure PCIE Hot Plug.

### PCIE1/PCIE2/PCIE3/PCIE4/PCIE6 Surprise Hot Plug

This option specifies if the link is considered Surprise Hot Plug capable.

### MCIO1\_1/MCIO1\_2/M2\_1/M2\_2 Surprise Hot Plug

This option specifies if the link is considered Surprise Hot Plug capable.

#### PCIF ASPM

Selec this item to disable ASPM support in all PCIe root ports.

#### PCIE1/PCIE2/PCIE3/PCIE4/PCIE6 ASPM Support

Use this item to disable ASPM support in PCIe root port. The default value is [Auto].

#### MCIO1\_1/MCIO1\_2/M2\_1/M2\_2 ASPM Support

Use this item to disable ASPM support in PCIe root port. The default value is [Auto].

## 3.3.5 NVMe Configuration



#### Launch NVMe driver

Select this item to enable or disable launch NVMe driver.

### Phison ESMP512GKB4C2-E16S

### **Self Test Option**

Select either Short or Extended Self Test. Short option will take couple of minutes and extended option will take several minutes to complete.

#### **Self Test Action**

Select either to test Controller alone or Controller and NameSpace. Selecting Controller and NameSpace option will take lot longer to complete the test.

#### Run Device Self Test

Perform device self test for the corresponding Option and Action selected by user. Pressing 'Esc' key will abort the test. Result shown below is the recent result logged in the device.

## 3.3.6 ACPI Configuration



#### PCIE Devices Power On

Allow the system to be waked up by a PCIE device and enable wake on LAN.

### Ring-In Power On

Allow the system to be waked up by onboard COM port modem Ring-In Singals.

#### RTC Alarm Power On

Allow the system to be waked up by real time clock alarm. Set it to By OS to let it be handled by the operating system.

#### RTC Alarm Date

Use this item to set Date of RTC power on feature.

#### RTC Alarm Hour

Use this item to set Hour of RTC power on feature.

#### **RTC Alarm Minute**

Use this item to set Minute of RTC power on feature.

### **RTC Alarm Second**

Use this item to set Second of RTC power on feature.

## 3.3.7 USB Configuration



This displays the USB Module Version, Controllers and USB Devices information.

## 3.3.8 Super IO Configuration



## Serial Port 1 Configuration

Use this item to set parameters of Serial Port 1 (COMA).

#### Serial Port

Use this item to enable or disable the Serial Port (COM).

#### **Change Settings**

Use this item to select an optimal setting for Super IO device.

## **SOL Configuration**

Use this item to set parameters of Serial Port 2 (COMB).

#### **SOL Port**

Use this item to enable or disable the Serial Port (COM).

#### **Change Settings**

Use this item to select an optimal setting for Super IO device.

## 3.3.9 Serial Port Console Redirection



#### COM1 / SOL

#### Console Redirection

Use this option to enable or disable Console Redirection. If this item is set to Enabled, user can select a COM Port to be used for Console Redirection.

## Console Redirection Settings

Use this option to configure Console Redirection Settings, and specify how the computer and the host computer to which are connected exchange information. Both computers should have the same or compatible settings.

## **Terminal Type**

Use this item to select the preferred terminal emulation type for out-of-band management. It is recommended to select [VT-UTF8].

Option	Description
VT100	ASCII character set
VT100Plus	Extended VT100 that supports color and function keys
VT-UTF8	UTF8 encoding is used to map Unicode chars onto 1 or more bytes
ANSI	Extended ASCII character set

#### Bits Per Second

Use this item to select the serial port transmission speed. The speed used in the host computer and the client computer must be the same. Long or noisy lines may require lower transmission speed. The options include [9600], [19200], [38400], [57600] and [115200].

#### **Data Bits**

Use this item to set the data transmission size. The options include [7] and [8] (Bits).

#### Parity

Use this item to select the parity bit. The options include [None], [Even], [Odd], [Mark] and [Space].

#### **Stop Bits**

The item indicates the end of a serial data packet. The standard setting is [1] Stop Bit. Select [2] Stop Bits for slower devices.

#### Flow Control

Use this item to set the flow control to prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a "stop" signal can be sent to stop the data flow. Once the buffers are empty, a "start" signal can be sent to restart the flow. Hardware flow uses two wires to send start/stop signals. The options include [None] and [Hardware RTS/CTS].

### VT-UTF8 Combo Key Support

Use this item to enable or disable the VT-UTF8 Combo Key Support for ANSI/VT100 terminals.

#### Recorder Mode

Use this item to enable or disable Recorder Mode to capture terminal data and send it as text messages.

### Resolution 100x31

Use this item to enable or disable extended terminal resolution support.

#### **Putty Keypad**

Use this item to select Function Key and Keypad on Putty.

# Serial Port for Out-of-Band Management/Windows Emergency Management Services (EMS)

#### Console Redirection FMS

Use this option to enable or disable Console Redirection EMS. If this item is set to Enabled, user can select a COM Port to be used for Console Redirection.

## Console Redirection Settings

Use this option to configure Console Redirection Settings, and specify how the computer and the host computer to which are connected exchange information.

#### **Out-of-Band Mgmt Port**

Microsof t Windows Emergency Management Services (EMS) allows for remote management of a Windows Server OS through a serial port.

#### **Terminal Type EMS**

Use this item to select the preferred terminal emulation type for out-of-band management. It is recommended to select [VT-UTF8].

Option	Description
VT100	ASCII character set
VT100+	Extended VT100 that supports color and function keys
VT-UTF8	UTF8 encoding is used to map Unicode chars onto 1 or more bytes
ANSI	Extended ASCII character set

#### Bits Per Second EMS

Use this item to select the serial port transmission speed. The speed used in the host computer and the client computer must be the same. Long or noisy lines may require lower transmission speed. The options include [9600], [19200], [57600] and [115200].

#### Flow Control EMS

Use this item to set the flow control to prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a "stop" signal can be sent to stop the data flow. Once the buffers are empty, a "start" signal can be sent to restart the flow. Hardware flow uses two wires to send start/stop signals. The options include [None], [Hardware RTS/CTS], and [Software Xon/Xoff].

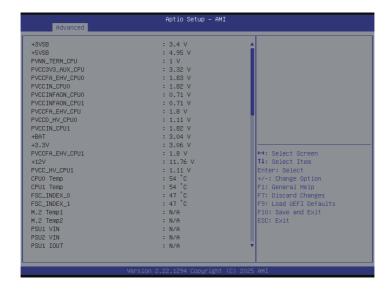
**Data Bits EMS** 

**Parity EMS** 

**Stop Bits EMS** 

### 3.3.10 H/W Monitor

In this section, it allows user to monitor the status of the hardware on the system, including the parameters of the CPU temperature, motherboard temperature, CPU fan speed, chassis fan speed, and the critical voltage.



# 3.3.11 Network Stack Configuration



#### Network Stack

Use this to enable or disable UEFI Network Stack.

### IPv4 PXE Support

Use this to enable or disable IPv4 PXE boot support. If disabled, IPv4 PXE boot support will not be available.

# IPv4 HTTP Support

Use this to enable or disable IPv4 HTTP boot support. If disabled, IPv4 HTTP boot support will not be available.

# IPv6 PXE Support

Use this to enable or disable IPv6 PXE boot support. If disabled, IPv6 PXE boot support will not be available.

# IPv6 HTTP Support

Use this to enable or disable IPv6 HTTP boot support. If disabled, IPv6 HTTP boot support will not be available.

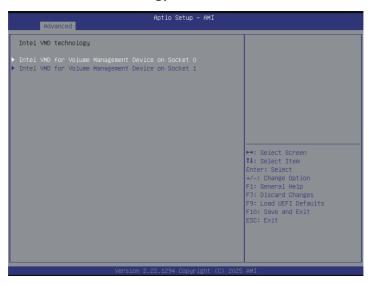
#### PXE boot wait time

Wait time in seconds to press ESC key to abort the PXE boot. Use either +/- or numeric keys to set the value.

# Media detect count

Number of times the presence of media will be checked. Use either +/- or numeric keys to set the value.

# 3.3.12 Intel VMD Technology



Press <Enter> to bring up the Intel VMD for Volume Management Device Configuration menu.

Intel VMD for Volume Management Device on Socket 0/1

# VMD Config for Stack 0 (PCIE4)/Stack 2 (PCIE6)/Stack 3 (PCIE2)/Stack 5 (MCIO1/M2)

Use this item to enable or disable Intel Volume Management Device Technology in this Stack.

When [Enabled] allowing user to configure the options below.

### VMD port A/B/E/G

Use this item to enable or disable Intel Volume Management Device Technology on specific root port.

### 3.3.13 Driver Health



Inter (R) PRO/1000 9.8.62 PCI-E Healthy

Provides Health Status for the Drivers/Controllers

Intel(R) 40GbE 4.5.05 Healthy

Provides Health Status for the Drivers/Controllers

# 3.3.14 Tls Auth Configuration



# Server CA Configuration

Press <Enter> to configure Server CA.

# Client Cert Configuration

#### **Enroll Cert**

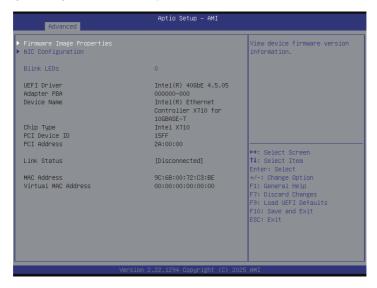
Press <Enter> to enroll cert.

#### **Delete Cert**

Press <Enter> to delete cert.

# 3.3.15 Intel(R) Ethernet Controller

Configure 10 Gigabit Ethernet device parameters.



### Firmware Image Properties

Select this item to view the device firmware version information.

# NIC Configuration

Click this item to configure the network device port.

#### Link Speed

Specifies the port speed used for the selected boot protocol.

#### Wake On LAN

Use this item to enable power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states.

#### **LLDP Agent**

Persistently enables or disables firmware's LLDP Agent. Note that disabling firmware's LLDP Agent also disables DCB functionality.

#### Blink LEDs

Identify the physical network port by blinking the associated LED.

# Port Configuration Information

Displays port configuration information including UEFI Driver, Adapter PBA, Device Name, Chip Type, PCI Device ID, PCI Address, Link Status, MAC Address and Virtual MAC Address.

# 3.3.16 VLAN Configuration



### **Enter Configuration Menu**

Press [Enter] to enter the menu for VLAN configuration.

#### VLAN ID

Specifies the VLAN ID of new VLAN or existing VLAN, the valid value is 0~4094.

#### **Priority**

Specifies the 802.1Q Priority, the valid value is  $0\sim7$ .

#### Add VLAN

Use this item to create a new VLAN or update existing VLAN.

#### Remove VLAN

Use this item to remove selected VLANS.

#### 3.3.17 Instant Flash

Instant Flash is a UEFI flash utility embedded in Flash ROM. This convenient UEFI updatetool allows user to update system UEFI without entering operating systems first like MSDOS or Windows\*. Just save the new UEFI file to the USB flash drive, floppy disk or hard drive and launch this tool, then update the UEFI only in a few clicks without preparing an additional floppy diskette or other complicated flash utility. Please be noted that the USB flash drive or hard drive must use FAT32/16/12 file system. Execute the Instant Flash utility, the utility will show the UEFI files and the respective information. Select the proper UEFI file to update UEFI, and reboot the system after the UEFI update process is completed.

# 3.4 Server Mgmt



#### Wait For BMC

Wait For BMC response for specified time out. BMC starts at the same time when BIOS starts during AC power ON. It takes around 255 seconds to initialize Host to BMC interfaces.

#### FRB-2 Timer

Use this item to enable or disable FRB-2 timer (POST timer)

#### FRB-2 Timer Timeout

Use this to define the FRB-2 Time Expiration between 1 to 30 value.

### FRB-2 Timer Policy

Configure how the system should respond. If the FRB-2 Timer expires is disabled, this item is not available.

# OS Watchdog Timer

Use this item to enable or disable OS Watchdog Timer. If enabled, starts a BIOS timer which can only be shut off by Management Software after the OS loads.

#### OS Wtd Timer Timeout

Configure the OS Boot Watchdog Timer Expiration between 1 to 30 min value. If the OS Boot Watchdog Timer is disabled, this item is not available.

### **OS Wtd Timer Policy**

Configure how the system should respond if the OS Boot Watchdog Timer expires. If the OS Boot Watchdog Timer is disabled, this item is not available.

# **BMC Network Configuration**

Use this to configure BMC network parameters.

### **DNS Configuration**

Use this to configure DNS parameters.

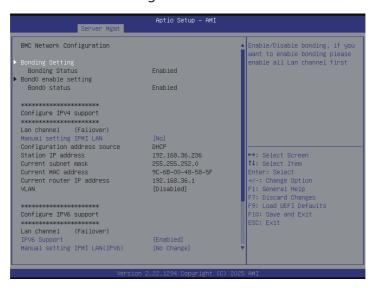
### System Event Log

Press <Enter> to change the SEL event log configuration.

#### **BMC Tools**

Use this item to configure about KCS control, restore AC power loss and load BMC default setings.

# 3.4.1 BMC Network Configuration



### **Bonding Setting**

Select this item to enabled or disabled bonding. Please enable all lan channel first when want to enable bonding.

# Manual Setting IPMI LAN

If [No] is selected, the IP address is assigned by DHCP. If using a static IP address, toggle to [Yes], and the changes take effect after the system reboots. The default value is [No].

# **Configuration Address Source**

Select to configure BMC network parameters statically or dynamically(by BIOS or BMC). Configuration options: [Static] and [DHCP].

**Static**: Manually enter the IP Address, Subnet Mask and Gateway Address in the BIOS for BMC LAN channel configuration.

**DHCP**: IP address, Subnet Mask and Gateway Address are automatically assigned by the network's DHCP server.



The default login information for the IPMI web interface is:

Username: admin Password: admin

For more instructions on how to set up remote control environment and use the IPMI management platform, please refer to the IPMI Configuration User Guide or go to the Support website at: http://www.asrockrack.com/support/faq.asp

#### VI AN

Enable or disable Virtual Local Area Network.

If [Enabled] is selected, configure the items below.

#### VLAN ID

Select this item to configure the VLAN ID setting, the Maximum value is 4094 and the Minimum value is 1.

#### **VLAN Priority**

Select this item to configure the VLAN Priotity setting. the Maximum value is 7 and the Minimum value is 0.

#### **IPV6 Support**

Enable or disable LAN IPV6 Support.

### Manual Setting IPMI LAN(IPV6)

Select to configure LAN channel parameters statically or dynamically(by BIOS or BMC). Unspecified option will not modify any BMC network parameters during BIOS phase.

# 3.4.2 DNS Configuration



### Manual DNS Configuration

Select this item to manual configure DNS.

If [YES] is selected, configure the items below.

#### **DNS Service**

Select this item to enable or disable DNS Service Configuration.

#### **Host Name Settings**

Select this item to automatic or manual Host Name Settings.

### **Bond Register BMC**

Use this item to enable or disable Bond Register BMC.

# **Bond Register Method**

Use this item to configure Bond Register Method with Nsupdate or DHCP client FQDN/ Hostname..

# Domain Setting

This item supports Manual, Bond0\_v4 and Bond0\_v6 Domain Settings.

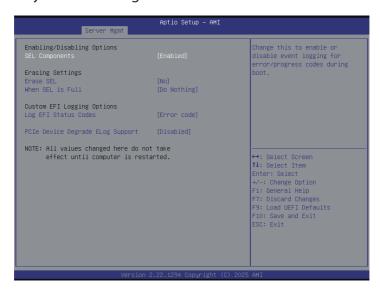
# Domain Name Server Setting

Select this item to configure DNS Server Settings.

# **IP Priority**

Select this item to configure IP Priority.

# 3.4.3 System Event Log



#### **SEL Components**

Change this to enable or disable event logging for error/progress codes during boot.

#### Frase SFI

Use this to choose options for earsing SEL.

#### When SEL is Full

Use this to choose options for reactions to a full SEL.

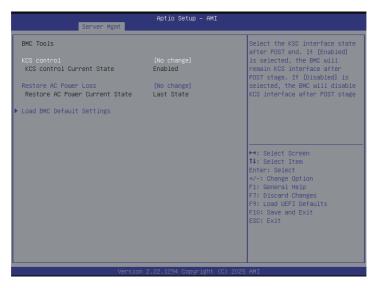
### Log EFI Status Codes

Use this item to disable the logging of EFI Status Codes or log only error code or only progress code or both.

# PCIe Device Degrade ELog Support

Use this item to enable or disable PCIe Device Degrade Error Logging Support.

#### 3.4.4 BMC Tools



#### KCS Control

Select this KCS interface state after POST end. If [Enabled] us selected, the BMC will remain KCS interface after POST stage. If [Disabled] is selected, the BMC will disable KCS interface after POST stage

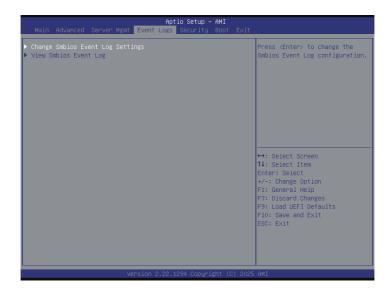
#### Restore AC Power Loss

This allows user to set the power state after an unexpected AC/power loss. If [Power Off] is selected, the AC/power remains off when the power recovers. If [Power On] is selected, the AC/power resumes and the system starts to boot up when the power recovers. If [Last State] is selected, it will recover to the state before AC/power loss.

# Load BMC Default Settings

Use this item to Load BMC Default Settings

# 3.5 Event Logs



### **Change Smbios Event Log Settings**

Use this item to configure the Smbios Event Log Settings.

When entering the item, the screen displays following sub-items:

#### **Smbios Event Log**

Use this item to enable or disable all features of the SMBIOS Event Logging during system boot

#### **Erase Event Log**

Choose options for erasing Smbios Event Log. Erasing is done prior to any logging activation during reset.

#### When Log is Full

Use this item to choose options for reactions to a full Smbios Event Log. The options include [Do Nothing] and [Erase Immediately].

#### Log System Boot Event

Choose option to enable/disable logging of System boot event.

#### View Smbios Event Log

Press <Enter> to view the Smbios Event Log records.



All values changed here do not take effect until computer is restarted.

# 3.6 Security

This section allows user to set or change the supervisor/user password for the system. For the user password item is allowed user to clear it.



### Supervisor Password

Set or change the password for the administrator account. Only the administrator has authority to change the settings in the UEFI Setup Utility. Leave it blank and press enter to remove the password.

#### User Password

Set or change the password for the user account. Users are unable to change the settings in the UEFI Setup Utility. Leave it blank and press enter to remove the password.

#### Secure Boot

Secure Boot feature is Active if Secure Boot is Enabled, Platform Key(PK) is enrolled and the System is in User mode. The mode change requires platform reset.

#### Secure Boot Mode

Secure Boot mode selector: Standard/Custom. In Custom mode Secure Boot Variables can be configured without authentication.

# Install Default Secure Boot Keys

Please install default secure boot keys if it's the first time useing secure boot.

# Clear Secure Boot Keys

Use this to clear the secure boot keys.

# **Expert Key Management**

Enables expert users to modify Secure Boot Policy variables without variable authentication.

# 3.6.1 Expert Key Management

In this section, expert user can modify Secure Boot Policy variables without full authentication.



### **Factory Key Provision**

Install factory default Secure Boot keys after the platform reset and while the System is in Setup mode.

# Install Default Secure Boot Keys

Please install default secure boot keys if it's the first time to use secure boot.

# Clear Secure Boot Keys

This force system to setup Mode-Clear all Secure Boot Variables. Change takes effect after reboot

# Enroll Efi Image

Allow the image to run in Secure Boot mode. Enroll SHA256 hash of the binary into Authorized Signature Database (db).

# **Export Secure Boot Variables**

Copy NVRAM content of Secure Boot variables to files in a root folder on a file system device.

### Platform Key (PK)

Enroll Factory Defaults or load certificates from a file:

- 1. Public Key Certificate in:
- a) EFI\_SIGNATURE\_LIST
- b) EFI\_CERT\_X509 (DER)
- c) EFI\_CERT\_RSA2048 (bin)
- d) EFI\_CERT\_SHAXXX
- 2. Authenticated UEFI Variable
- 3. EFI PE/COFF Image(SHA256)

Key Source: Factory, Modified, Mixed

### Key Exchange Keys (KEK)

Enroll Factory Defaults or load certificates from a file:

- 1. Public Key Certificate in:
- a) EFI\_SIGNATURE\_LIST
- b) EFI\_CERT\_X509 (DER)
- c) EFI\_CERT\_RSA2048 (bin)
- d) EFI\_CERT\_SHAXXX
- 2. Authenticated UEFI Variable
- 3. EFI PE/COFF Image(SHA256)

Key Source: Factory, Modified, Mixed

### Authorized Signatures (db)

Enroll Factory Defaults or load certificates from a file:

- 1. Public Key Certificate in:
- a) EFI\_SIGNATURE\_LIST
- b) EFI\_CERT\_X509 (DER)
- c) EFI\_CERT\_RSA2048 (bin)
- d) EFI\_CERT\_SHAXXX

- 2. Authenticated UEFI Variable
- 3. EFI PE/COFF Image(SHA256)

Key Source: Factory, Modified, Mixed

#### Forbidden Signatures (dbx)

Enroll Factory Defaults or load certificates from a file:

- 1. Public Key Certificate in:
- a) EFI\_SIGNATURE\_LIST
- b) EFI\_CERT\_X509 (DER)
- c) EFI\_CERT\_RSA2048 (bin)
- d) EFI\_CERT\_SHAXXX
- 2. Authenticated UEFI Variable
- 3. EFI PE/COFF Image(SHA256)

Key Source: Factory, Modified, Mixed

#### Authorized TimeStamps (dbt)

Enroll Factory Defaults or load certificates from a file:

- 1. Public Key Certificate in:
- a) EFI\_SIGNATURE\_LIST
- b) EFI\_CERT\_X509 (DER)
- c) EFI\_CERT\_RSA2048 (bin)
- d) EFI\_CERT\_SHAXXX
- 2. Authenticated UEFI Variable
- 3. EFI PE/COFF Image(SHA256)

Key Source: Factory, Modified, Mixed

# OsRecovery Signatures (dbr)

Enroll Factory Defaults or load certificates from a file:

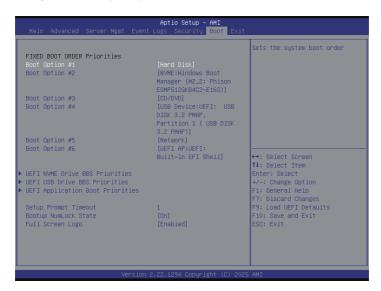
- 1. Public Key Certificate in:
- a) EFI\_SIGNATURE\_LIST

- b) EFI\_CERT\_X509 (DER)
- c) EFI\_CERT\_RSA2048 (bin)
- d) EFI\_CERT\_SHAXXX
- 2. Authenticated UEFI Variable
- 3. EFI PE/COFF Image(SHA256)

Key Source: Factory, Modified, Mixed

#### 3.7 Boot Screen

In this section, it will display the available devices on the system for user to configure the boot settings and the boot priority.



### Boot Option #1/#2/#3/#4/#5/#6

Use this to set the system boot order.

#### **UEFI NVME Drive BBS Priorities**

Specifies the Boot Device Priority sequence from available UEFI NVME Drives.

#### **UFFI USB Drive BBS Priorities**

Specifies the Boot Device Priority sequence from available UEFI USB Drives.

#### **UEFI Application Boot Priorities**

Specifies the Boot Device Priority sequence from available UEFI Application.

# Setup Prompt Timeout

Configure the number of seconds to wait for the UEFI setup utility.

# **Bootup NumLock State**

Select whether Num Lock should be turned on or off when the system boots up.

# Full Screen Logo

Enable to display the boot logo or disable to show normal POST message.

#### 3.8 Exit Screen



### Save Changes and Exit

When selecting this option, the following message "Save configuration changes and exit setup?" will pop-out. Press <F10> key or select [Yes] to save the changes and exit the UEFI SETUP UTILITY

# Discard Changes and Exit

When selecting this option, the following message "Discard changes and exit setup?" will pop-out. Press <ESC> key or select [Yes] to exit the UEFI SETUP UTILITY without saving any changes.

# Save Changes

When selecting this option, the following message "Save changes?" will pop-out. Select [Yes] to save all changes.

# **Discard Changes**

When selecting this option, the following message "Discard changes?" will pop-out. Select [Yes] to discard all changes.

#### Load UEFI Defaults

Use this item to restore or load default values for all the setup options.

# **Chapter 4 Software Support**

After all the hardware has been installed, it suggests to go to the offical website at <a href="http://www.ASRockRack.com">http://www.ASRockRack.com</a> and make sure if there are any new updates of the BIOS / BMC firmware for the motherboard.

# 4.1 Download and Install Operating System

This motherboard supports various Microsoft\* Windows\* Server / Linux compliant operating systems. Please download the operating system from the OS manufacturer. Please refer to the OS documentation for more instructions.

\*Please download the Intel\* Rapid Storage Technology driver from the ASRock Rack's website (www.asrockrack.com) to your USB drive while installing OS in SATA RAID mode.

### 4.2 Download and Install Software Drivers

This motherboard supports various Microsoft\* Windows\* compliant drivers. Please download the required drivers from the website at <a href="http://www.ASRockRack.com">http://www.ASRockRack.com</a>.

To download necessary drivers, go to the product page, click on the "Download" tab, choose the operating system that is used, and then download the using driver.

### 4.3 Contact Information

Contact ASRock Rack or want to know more about ASRock Rack, welcome to visit ASRock Rack's website at <a href="http://www.ASRockRack.com">http://www.ASRockRack.com</a>; or contact the dealer for further information.

# **Chapter 5 Troubleshooting**

# 5.1 Troubleshooting Procedures

Follow the procedures below to troubleshoot the system.



Always unplug the power cord before adding, removing or changing any hardware components. Failure to do so may cause physical injuries and damages to motherboard components.

- 1. Disconnect the power cable and check whether the PWR LED is off.
- Unplug all cables, connectors and remove all add-on cards from the motherboard. Make sure that the jumpers are set to default settings.
- 3. Confirm that there are no short circuits between the motherboard and the chassis.
- 4. Install a CPU and fan on the motherboard, then connect the chassis speaker and power LED

#### If there is no power...

- 1. Confirm that there are no short circuits between the motherboard and the chassis.
- 2. Make sure that the jumpers are set to default settings.
- 3. Check the settings of the 115V/230V switch on the power supply.
- Verify if the battery on the motherboard provides ~3VDC. Install a new battery if it does not.

#### If there is no video...

- 1. Try replugging the monitor cables and power cord.
- 2. Check for memory errors.

#### If there are memory errors...

- 1. Verify that the DIMM modules are properly seated in the slots.
- 2. Use recommended DDR5 RDIMM.
- Install more than one DIMM modules that should be identical with the same brand, speed, size and chip-type.
- 4. Try inserting different DIMM modules into different slots to identify faulty ones.
- 5. Check the settings of the 115V/230V switch on the power supply.

### Unable to save system setup configurations...

- 1. Verify if the battery on the motherboard provides ~3VDC. Install a new battery if it does not.
- 2. Confirm whether the power supply provides adaquate and stable power.

### Other problems...

1. Try searching keywords related to the related problem on ASRock Rack's FAQ page: http://www.asrockrack.com/support

# 5.2 Technical Support Procedures

If the problems are still unsolved, please contact ASRock Rack's technical support with the following information:

- 1. Contact information
- 2. Model name, BIOS version and problem type.
- 3. System configuration.
- 4. Problem description.

Contact ASRock Rack's technical support at: http://www.asrockrack.com/support/tsd.asp

# 5.3 Returning Merchandise for Service

For warranty service, the receipt or a copy of the invoice marked with the date of purchase is required. By calling the vendor or going to RMA website (http://event. asrockrack.com/tsd.asp) to obtain a Returned Merchandise Authorization (RMA) number.

The RMA number should be displayed on the outside of the shipping carton which is mailed prepaid or hand-carried when returning the motherboard to the manufacturer. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

This warranty does not cover damages incurred in shipping or from failure due to alteration, misuse, abuse or improper maintenance of products.

Contact the distributor first for any product related problems during the warranty period.

### **Contact Information**

If it needs to contact ASRock Rack or want to know more about ASRock Rack, you're welcome to visit ASRock Rack's website at http://www.asrockrack.com; or contact the dealer for further information. For technical questions, please submit a support request form at https://event.asrockrack.com/tsd.asp

### **ASRock Rack Incorporation**

e-mail: ASRockRack\_sales@asrockrack.com

#### ASRock Rack EUROPE B.V.

Bijsterhuizen 11-11 6546 AR Nijmegen The Netherlands

Phone: +31-24-345-44-33

#### ASRock Rack America, Inc.

13848 Magnolia Ave, Chino, CA91710 U.S.A.

Phone: +1-909-590-8308 Fax: +1-909-590-1026